

ABSTRACT

In an active matrix type liquid crystal display device, a time gradation display manner for performing time gradation display is used. A digital gradation (voltage) signal on a signal line is supplied to the digital memory circuit arranged in vicinity of each pixel electrode and stored therein for a desired period of time. This storage state is held until next scanning is started. A high voltage or a low voltage supplied as a power source voltage of the digital memory circuit is applied to a pixel electrode while the digital memory circuit is in a storage state, so that a desired voltage is stably applied to the pixel electrode.

003030 E 9 F 34 9 30